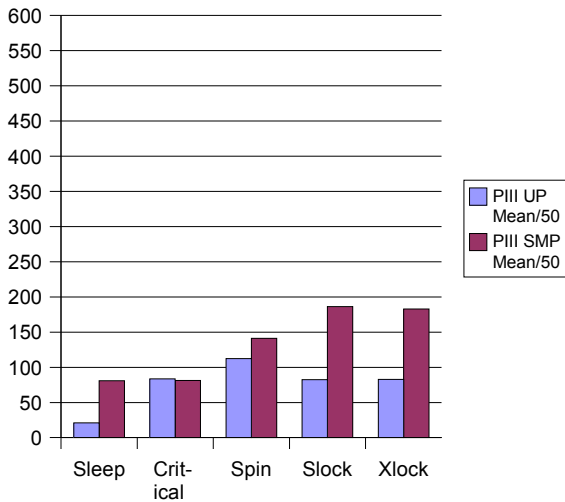Comparison in the costs of synchronization primitives on a P4 Xeon 3Ghz and PIII 800 Mhz
All periods measured in #cycles

Type	PIII UP Mean/50	PIII SMP Mean/50	P4 UP Mean/50	P4 SMP Mean/50
Sleep	21.07	80.98	38.5	260.28
Critical	83.36	81.38	120.13	119.29
Spin	112.46	141.26	273.66	342.48
Slock	82.46	186.2	92.8	549.32
Xlock	82.61	182.84	98.4	567.08

Measurements performed by reading the cycle counter before and after a loop of
50,000 acquisition and release pairs for the primitive.

Five samples are taken, the mean of the results is presented, divided by 50,000
to generate the average cycles/operation pair.

PIII Synchronization

P4 Synchronization